

U.S. Department of Commerce, Patent and Trademark Office

Attorney Docket No.: 026-0015

Application No.: 09/888,708

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Applicant(s): Michael H. Perrott

(Use several sheets if necessary)

Filing Date: June 25, 2001

APR 12 2002

Group Art Unit: 2133

Date Submitted: April 9, 2002

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name
AY	AA	6,137,372	Oct. 24, 2000	Welland
	AB			
	AC			
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	AH			
	AI			
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	AK			

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Foreign Patent Documents

					Translation	
		Document	Date	Country	Yes	No
	AL					
	AM					
	AN					
	AP					

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AY	AR	Giga, "2.5 Gbit/s Clock and Data Recovery GD16522", Data Sheet Rev. 20, Giga, September 25, 2000, pp. 1-11
AY	AS	Maxim, "2.5Gbps, Low-Power, +3.3V Clock Recovery and Data Retiming IC", Maxim Integrated Products, 1998, pp. 1-8
AY	AT	Gutierrez, German, et al, "2.488 Gb/s Silicon Bipolar Clock and Data Recovery IC for SONET (OC-48)", IEEE Custom Integrated Circuits Conference 1998, pp. 575-578

Examiner

Date Considered

10/9/03

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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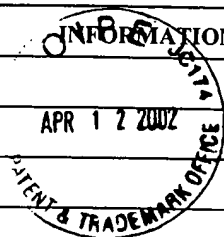
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AY	AR	Gutierrez, German, et al, "Unaided 2.5 Gb/s Silicon Bipolar Clock and Data Recovery IC", IEEE Radio Frequency Integrated Circuits Symposium 1998, pp. 173-176
AY	AS	Silicon Laboratories, "SiPHYTM Multi-Rate SONET/SDH Clock and Data Recovery IC", Si5020-DS06, Preliminary Rev. 0.6 7/00, pp. 1-16
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Examiner

Date Considered

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